



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2183
JFW

Applicant: Roger A. Bethard

Title: SYSTEM AND METHOD FOR ADDRESSING MEMORY AND TRANSFERRING DATA

Docket No.: 1376.689US1

Serial No.: 10/037,479

Filed: October 24, 2001

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2183

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.
 An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 4 cited documents.
 Partial European Search Report from European Application No. 02257325.7 (4 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By:

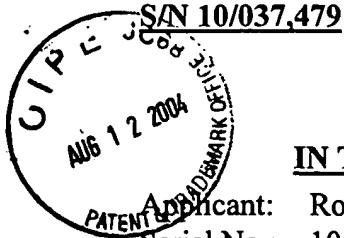
Atty: Thomas F. Brennan
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9 day of August, 2004.

Thomas F. Brennan
Name

Thomas F. Brennan
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Roger A. Bethard Examiner: Unknown
Serial No.: 10/037,479 Group Art Unit: 2183
Filed: October 24, 2001 Docket: 1376.689US1
Title: SYSTEM AND METHOD FOR ADDRESSING MEMORY AND
TRANSFERRING DATA

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

The attached documents were discovered as a result of a Search Report in Applicants' corresponding foreign patent application. Enclosed for the Examiner's information is a copy of the cited documents and the Search Report.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No :10/037,479

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Title: SYSTEM AND METHOD FOR ADDRESSING MEMORY AND TRANSFERRING DATA

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Dkt: 1376.689US1

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

ROGER A. BETHARD

By his Representatives,

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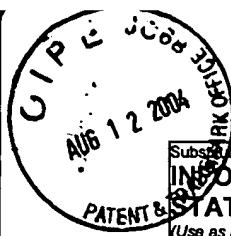
Date August 7, 2004

By Thomas F. Brennan
Thomas F. Brennan
Reg. No. 35,075

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PAMELA J. RYAN
Name

ROGER A. BETHARD
Signature



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		<i>Complete if Known</i>	
		Application Number	10/037,479
		Filing Date	October 24, 2001
		First Named Inventor	Bethard, Roger
		Group Art Unit	2183
		Examiner Name	Unknown
Sheet 1 of 1		Attorney Docket No: 1376.689US1	

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,255,384	10/19/1993	Sachs, H. G., et al.	711	207	09/26/1991
	US-6,182,195	01/30/2001	Laudon, J. P., et al.	711	141	07/28/1998

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	EP-0766177A1	04/02/1997	Kaiser, J. M.	G06F	12/10	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		TELLER, P. J., et al., "Locating Multiprocessor TLBs at Memory", <u>Proceedings of the Twenty-Seventh Hawaii International Conference on Wailea, Vol. I, 1) IEEE Computer Society 2) System Sciences, Architecture, (01/04/1994), 554-563</u>	

EXAMINER

DATE CONSIDERED